

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. ISHIBASHI, et al.  
Application No.: Rule 1.53(b) Continuation of U.S. Patent Application  
Serial No. 09/780,393, filed February 12, 2002  
Filed: On even date herewith  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
Art Unit in parent: 2824  
Examiner in parent: R. Elms

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 25, 2003

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."


Applicants are submitting herewith a copy of Form PTO-1449 which list documents cited in parent application Serial No. 09/780,393.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (520.39604CX1).

Respectfully submitted,

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>			
		Application Number	Not Yet Assigned		
		Filing Date	On Even Date Herewith		
		First Named Inventor	K. ISHIBASHI, et al.		
		Art Unit	Not Yet Assigned		
		Examiner Name	Not Yet Assigned		
Sheet	1	of	1	Attorney Docket Number	520.39604CX1

U.S. PATENT DOCUMENTS						
Examiner Initials'	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
		US-	5,465,231	11-1995	Ohsaki	
		US-	5,457,335	10-1995	Kuroda et al.	
		US-	5,163,023	11-1992	Ferris et al.	
		US-	5,089,433	02-1992	Anand et al.	
		US-	4,794,597	12-1988	Ooba et al.	
		US-	6,115,293	09-2000	M. Yamashita	
		US-				
		US-				

FOREIGN PATENT DOCUMENTS						
Examiner Initials'	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		JP 7287994	10-1995			
		JP 785692	03-1995			
		JP 60201599	10-1985			
		JP 61123100	06-1986			
		05298888	11-1993			
		JP 5114300	05-1993			
		JP 10149694	06-1998			
		JP 5314789	11-1993			
		1197652	01-1999			

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		Takai et al., "A 250Mv/s/pin 1Gb Double Data Rate SDRAM with a Bi-Directional Delay and an Inter-Bank Shared Redundancy Scheme", 1999 IEEE Int'l Solid-State Circuits Conference, pp. 418-419	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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